	ent No:		
	C.U.SHAF	H UNIVERSITY	
	Winter E	xamination-2015	
Subject	Name : Microcontroller & Inter	rfacing	
Subject	Code :4TE04MCI1	Branch : B.Tech (EE, I	C)
Semeste Instruction		Time :2:30To5:30	Marks:70
(2) (3)	Instructions written on main answer Draw neat diagrams and figures (in Assume suitable data if needed.	•	r
a)	Attempt the following question In 8051 an external interrupt 1 vinterrupt if a) 000BH, a high to low transition to 001BH, a low to high transition c) 0013H, a high to low transition d) 0023H, a low to high transition	vector address is of and ca on on pin INT1 ion on pin INT1 on on pin INT1	auses of
1 \	u) 002311, a 10w to mgn transmo	on on bin nat i	
b)	Serial port vector address is of _a) 0013H, either TI or RI flag is b) 0023H, either TI or RI flag is c) 0013H, either TI or RI flag is d) 0023H, either TI or RI flag is	. And causes an interrupt what is reset is reset is set	
b) с)	Serial port vector address is of _a) 0013H, either TI or RI flag is b) 0023H, either TI or RI flag is c) 0013H, either TI or RI flag is d) 0023H, either TI or RI flag is	And causes an interrupt what is set as reset as reset as set erface which line will instruct the Lo	

- c) none of the mentioned d) both of the mentioned
- e) 8051 series has how many 16 bit registers?
 - a) 2 b) 3
 - c) 1 d) 0
- **f)** When the micro controller executes some arithmetic operations, then the flag bits of which register are affected?
 - a) PSW b) SP
 - c) DPTR d) PC



g)	How are the bits of the register PSW affected if we select Bank2 of 8051? a) PSW.5=0 and PSW.4=1 b) PSW.2=0 and PSW.3=1	
	c) PSW.3=1 and PSW.4=1 d) PSW.3=0 and PSW.4=1	
h)	On power up, the 8051 uses which RAM locations for register R0- R7	
,	a) 00-2F b) 00-07	
	c) 00-7F d) 00-0F	
i)	How many bytes of bit addressable memory is present in 8051 based micro	
	controllers?	
	a) 8 bytes b) 32 bytes	
	c) 16 bytes d) 128 bytes	
j)	In serial communication modes, mode 1 the Baud rate =	
	a) BR=2SMOD/32 * (Timer 0 over flow rate)	
	b) BR=2SMOD/16 * (Timer 1 over flow rate)	
	c) BR=2SMOD/16 * (Timer 0 over flow rate)	
	d) BR=2SMOD/32 * (Timer 1 over flow rate)	
k)	How is the status of the carry, auxiliary carry and parity flag affected if write	
	instruction NOV A 1900	
	MOV A,#9C	
	ADD A,#64H	
	a) CY=0,AC=0,P=0 b) CY=1,AC=1,P=0	
1)	c) CY=0,AC=1,P=0 d) CY=1,AC=1,P=1	
l)	The transmit buffer of serial data buffer is a	
	a) serial-in parallel-out registerb) parallel-in serial-out registerc) serial-in serial-out registerd) parallel-in parallel-out register	
m)	The register that can be used as a scratch pad is	
111)	a) Accumulator b) B register	
	c) Data register d) Accumulator and B register	
n)	The register that provides control and status information about serial port is	
11)	a) IP b) IE	
	c) TSCON d) PCON and SCON	
t any f	our questions from Q-2 to Q-8	
	Attempt all questions	(14)
(A)	Explain the difference between Microcontroller and Microprocessor?	(07)
(B)	Enlist various Addressing modes of 8051 Microcontroller and explain any one in detail?	(07)
	Attempt all questions	(14)
(A)	Draw and explain Pin diagram of 8051 microcontroller?	(07)
(B)	Explain the interfacing of RS 232 and 8051 Microcontroller?	(07)
	Attempt all questions	(14)
(A)	Draw and explain the Architecture of 8051 Microcontroller?	(07)
(B)	What is the difference between jump and call and also describe different types of jump and call instruction with its range?	(07)
	Attempt all questions	(14)
(A)	Design a counter for counting the pulses of an input signals. The pulses to be counted are	(07)

Attempt Q-2

Q-3

Q-4

Q-5

(B)



Explain Serial Programming of 8051 with suitable example to receive and

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(07)

fed to Pin p3.4. XTAL=22MHz?

		transfer the data?	
Q-6		Attempt all questions	(14)
	(A)	Explain Bit Addressable Memory of 8051 and its application for interfacing with suitable example?	(07)
	(B)	Write a Short note on Flash Controller and its applications?	(07)
Q-7		Attempt all questions	(14)
	(A)	Draw the schematic to interface Stepper Motor with 8051 Microcontroller and also write down the corresponding program for interface?	
Q-8		Attempt all questions	(14)
	(A)	Draw and explain architecture of 8085 and explain its memory interfacing with suitable example?	